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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/706,653	11/12/2003	Shaun Kazuo Wakumoto	200313912-1	7013	
22879 HEWI ETT P <i>A</i>	7590 11/28/2007 ACKARD COMPANY	•	EXAMINER		
P O BOX 272400, 3404 E. HARMONY ROAD			ZAIDI, SYED		
	LECTUAL PROPERTY ADMINISTRATION COLLINS, CO 80527-2400		ART UNIT	PAPER NUMBER	
	•		2616		
			MAIL DATE	DELIVERY MODE	
	•		11/28/2007	PAPER	

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

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••	Application No.	Applicant(s)				
	10/706,653	WAKUMOTO ET ĄL.				
Office Action Summary	Examiner	Art Unit				
	Syed Zaidi	2616				
The MAILING DATE of this communication ap Period for Reply	opears on the cover sheet w	ith the correspondence address				
A SHORTENED STATUTORY PERIOD FOR REPLY WHICHEVER IS LONGER, FROM THE MAILING I Extensions of time may be available under the provisions of 37 CFR 1 after SIX (6) MONTHS from the mailing date of this communication. If NO period for reply is specified above, the maximum statutory period Failure to reply within the set or extended period for reply will, by statu Any reply received by the Office later than three months after the mailine earned patent term adjustment. See 37 CFR 1.704(b).	DATE OF THIS COMMUNI .136(a). In no event, however, may a d will apply and will expire SIX (6) MOI tte, cause the application to become A	CATION. reply be timely filed NTHS from the mailing date of this communication. BANDONED (35 U.S.C. § 133).				
Status						
1) Responsive to communication(s) filed on 13.	January 2007.					
2a) ☐ This action is FINAL . 2b) ☑ Th						
3) Since this application is in condition for allow	·	•				
closed in accordance with the practice under	Ex parte Quayle, 1935 C.	D. 11, 453 O.G. 213.				
Disposition of Claims						
4)⊠ Claim(s) <u>1-7,9-11 and 13</u> is/are pending in th	e application.					
4a) Of the above claim(s) 8 and 12 is/are with	ndrawn from consideration.					
5) Claim(s) is/are allowed.						
6)⊠ Claim(s) <u>1-7,9-11 and 13</u> is/are rejected.						
7) Claim(s) is/are objected to.						
8) Claim(s) are subject to restriction and	or election requirement.					
Application Papers						
9)☐ The specification is objected to by the Examir	ner.					
10)⊠ The drawing(s) filed on <u>12 November 2003</u> is	/are: a)⊠ accepted or b)[objected to by the Examiner.				
Applicant may not request that any objection to th	e drawing(s) be held in abeya	nce. See 37 CFR 1.85(a).				
Replacement drawing sheet(s) including the corre).			
11) The oath or declaration is objected to by the E	Examiner. Note the attache	d Office Action or form PTO-152.				
Priority under 35 U.S.C. § 119						
12) ☐ Acknowledgment is made of a claim for foreig	n priority under 35 U.S.C.	§ 119(a)-(d) or (f).				
1. Certified copies of the priority documer						
2. Certified copies of the priority documer		· ·				
3. Copies of the certified copies of the pri	•	received in this National Stage				
application from the International Bure * See the attached detailed Office action for a lis	* **	received				
dec inc dilached detailed office detail for a lie	st of the defined dopies he	. Todowod.				
Attachment(s)	" –					
1) Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTØ-948)		Summary (PTO-413) (s)/Mail Date				
3) Information Disclosure Statement(s) (PTO/SB/08) Paper No(s)/Mail Date		Informal Patent Application				

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DETAILED ACTION

Response to Arguments

Applicant's arguments filed August 29, 2007 have been fully considered but they are moot, with respect to the rejection of claims 1-13. In view of new grounds of rejection been presented in this office action as such may response to applicant's argument is moot.

Claims 1, 9, 11 and 13 have been amended.

Claims 8 and 12 have been cancelled.

Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

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The factual inquiries set forth in *Graham* v. *John Deere Co.*, 383 U.S. 1, 148 USPQ 459 (1966), that are applied for establishing a background for determining obviousness under 35 U.S.C. 103(a) are summarized as follows:

- 1. Determining the scope and contents of the prior art.
- 2. Ascertaining the differences between the prior art and the claims at issue.
- 3. Resolving the level of ordinary skill in the pertinent art.
- 4. Considering objective evidence present in the application indicating obviousness or nonobviousness.

This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

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Claims 1- 7 and 9-13 are rejected under 35 U.S.C. 103(a) as being unpatentable over Bare. (U.S.Patent # 6,947,384 B2) in further view of Augart. (U.S. Patent Number 6,778,524 B1).

Consider claim 1, Bare. discloses and shows a method of automated path tracing from an original mesh switch (column 9 lines 15-20, figure # 1) through a switching mesh to a specified destination (column 9 lines 15-20, figure # 1) the method comprising, building a mesh trace route packet to the specified destination (column 9 lines 44-47, figure # 1) transmitting the mesh trace route packet via an exit port associated with the specified destination (column 12 lines 33-48 figure # 16) and receiving the mesh trace route packet as returned (column 12 lines 44-58, figure # 16) wherein the mesh trace route packet as returned includes a plurality of hop entries providing a path trace from the original mesh switch through the switching mesh to the specified destination. However Bare, fails to each hop entry including a hop media access (MAC) address a hop in-port, and a hop out-port.

In the same field of endeavor, **Augart.** show and disclose each hop entry including a hop media access (MAC) address a hop in-

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port, and a hop out-port (column 7 lines 13-20, column 8 lines 48-55, figure # 1).

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of invention was made to incorporate the each hop entry including a hop media access (MAC) address a hop in-port, and a hop out-port as taught by **Augart.** in the method of **Bare** to allow data access in the event of route failures.

Consider claim 2, as applied to claim 1 above, Bare. and as modified by Augart. and as modified by Augart., clearly shows and discloses the method, wherein the specified destination comprises a search MAC address (a MAC address for tracing route) and VLAN identifier (column 12 lines 33-48, figure #)23, 29, 30).

Consider claim 3 as applied to claim 1 above, Bare and as modified by Augart. clearly shows and discloses the method, further comprising, determining whether a trace complete flag (sequence number flag) in the returned packet is set (column 37 lines 6-47); and out putting results from a completed mesh traceroute if the trace

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complete flag is set and if a trace found flag is set. However **Bare.**fails to completed mesh traceroute if the trace complete flag is set
and if a trace found flag is set.

In the same field of endeavor, **Augart.** show and disclose each completed mesh traceroute if the trace complete flag is set and if a trace found flag is set. (column 7 lines 50-65, figure # 1).

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of invention was made to incorporate the completed mesh trace route if the trace complete flag is set and if a trace found flag is set as taught by **Augart.** in the method of **Bare** to allow data access in the event of mesh trace route flag as described by **Bare.** in the cited passage.

Consider claim 4, as applied to claim 3 above, Bare and as modified by Augart. and clearly shows and discloses the method that generate an error message, if the trace complete flag is clear if failure is indicated by another flag (switch query error message) (column 21 lines 55-60, figure # 8).

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Consider claim 5, as applied to claim 1 above, Bare and as modified by Augart. clearly shows and discloses the method, further comprising: receiving the mesh trace route packet at a hop mesh switch (column 48 lines 19-54) appending a hop entry to the mesh trace route packet and forwarding the packet via a hop out-port to a next mesh switch. However Bare, fails to completed hop entry to the mesh trace route packet and forwarding the packet via a hop out-port to a next mesh switch.

In the same field of endeavor, **Augart.** shows and discloses hop entry to the mesh trace route packet and forwarding the packet via a hop out-port to a next mesh switch (column 8 lines 47-65, figure # 1).

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of invention was made to incorporate hop entry to the mesh trace route packet and forwarding the packet via a hop out-port to a next mesh switch as taught by **Augart.** in the method of **Bare.** to allow data access in the event of route failures.

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Consider claim 6, as applied to claim 5 above, Bare and as modified by Augart. clearly shows and discloses the method (column 12 lines 33-48 and figure # 1) further comprising: receiving the mesh trace route packet at a destination mesh switch (column 8 lines 16-19 and figure # 29) appending a final hop entry to the mesh trace route packet marking a trace complete flag and sending the packet back towards the original mesh (known as a looping) switch. However Bare. fails to each hop entry including a hop media access (MAC) address a hop in-port, and a hop out-port.

In the same field of endeavor, **Augart.** shows and discloses each hop entry including a hop media access (MAC) address a hop in-port, and a hop out-port (column 3 lines 65-67, column 4 lines 1-28, figure # 1).

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of invention was made to incorporate the each hop entry including a hop media access (MAC) address a hop in-port, and a hop out-port as taught by **Augart.** in the method of **Bare.** to allow data access in the event of (MAC) address a hop in-port, and a hop out-port.

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Consider claim 7, as applied to claim 1 above, Bare and as modified by Augart. clearly shows and discloses the method, wherein the packet is sent back towards the original mesh switch by way of a reverse trace path (column 9 lines 15-21, and figure # 1).

Consider claim 9, Bare a switching device configured to be a member of a switching mesh, the switching device comprising: a plurality of ports (column 11 lines 18-34 and figure # 32) and a switch control device coupled to the plurality of ports (column 5 lines) 20-30 and figure # 2, 3) wherein the switch control device is configured to perform an automated method of tracing a path through the switching mesh to a specified destination (column 12 lines 33-48 and figure # 32) wherein the automated method is accomplished by building a mesh trace route packet to the specified destination (column 11 lines 21-34 and figure # 32) transmitting the mesh trace route packet from an exit port associated with the specified destination (column 12 lines 1-3 and figure # 32), and receiving the mesh trace route packet as returned via the same port,

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port.

wherein the mesh trace route packet as returned includes a plurality of hop entries providing a path trace from the original mesh switch through the switching mesh to the specified destination, each hop entry including a hop media access (MAC) address, a hop in-port, and a hop out-port. However **Bare**. fails to each hop entry including a hop media access (MAC) address a hop in-port, and a hop out-

In the same field of endeavor, **Augart.** shows and discloses each hop entry including a hop media access (MAC) address a hop in-port, and a hop out-port (column 3 lines 65-67, column 4 lines 1-28, figure # 1).

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of invention was made to incorporate the each hop entry including a hop media access (MAC) address a hop in-port, and a hop out-port as taught by **Augart.** in the method of **Bare.** to allow data access in the event of routing loops failures.

Consider claim 10, as applied to claim 9 above, Bare and as modified by Augart. clearly shows and discloses the method,

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wherein the specified destination comprises a search MAC address (MAC address for tracing route as specified by the applicant) and VLAN identifier (column 6 lines 23-58, column 12 lines 33-48, figure # 23, 29, 30).

Consider claim, 11, Bare. clearly shows and discloses a method of responding to receipt of a mesh trace route packet during an automated path tracing, the method comprising (column11 lines) 28-33 and figure # 32) receiving the mesh trace route packet at a mesh switch; and if the mesh switch is determined to comprise a hop mesh switch, then appending a hop entry to the mesh trace route packet (column 11 lines 51-67 and figure # 32, wherein the hop entry includes at least a hop media access (MAC) address, a hop in-port, and a hop out-port, and forwarding the packet via the hop out-port to a next mesh switch. However Bare, fails to each hop entry including a hop media access (MAC) address a hop in-port, and a hop outport.

In the same field of endeavor, Augart. shows and discloses each hop entry including a hop media access (MAC) address a hop

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in-port, and a hop out-port (column 7 lines 13-20, column 8 lines 48-55, figure # 1).

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of invention was made to incorporate the each hop entry including a hop media access (MAC) address a hop in-port, and a hop out-port as taught by **Augart.** in the method of **Bare** to allow hop access in the event of route failures.

Consider claim 13, Bare and as applied to claim 11 above, clearly shows and discloses the method, further comprising: if the mesh switch is determined to comprise a destination mesh switch[[;]] then filling in at least a hop in-port in the hop entry[[;]], marking a trace complete flag[[;]], and returning the packet towards the original mesh switch via the hop in-port. However Bare. fails to each hop entry including a hop media access (MAC) address a hop in-port, and a hop out-port.

In the same field of endeavor, **Augart.** shows and discloses each hop entry including a hop media access (MAC) address a hop

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in-port, and a hop out-port (column 7 lines 13-20, column 8 lines 48-55, figure # 1).

Therefore, it would have been obvious to a person of ordinary skill in the art at the time of invention was made to incorporate the each hop entry including a hop media access (MAC) address a hop in-port, and a hop out-port as taught by **Augart.** in the method of **Bare** to allow data access in the event of drive failures as described by **Bare.** in the cited passage.

Conclusion

The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. **Eriksson.** (US Pub #2005/0013297 A1) discloses an control system and communication systems that make it possible to transport traffic in connection-oriented mode using the network infrastructure and hardware of a traditionally connectionless network. **Yu Shaohua** (US Patent #7,031,341 B2) discloses an interfacing apparatus and method for adopting Ethernet directly to physical channel.

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Any response to this Office Action should be **faxed to** (571)

273-8300 or mailed to:

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Hand-delivered responses should be brought to

Customer Service Window Randolph Building 401 Dulany Street Alexandria, VA 22314

Any inquiry concerning this communication or earlier communications from the Examiner should be directed to Syed Zaidi whose telephone number is (571) 270-1779. The Examiner can normally be reached on Monday-Thursday from 6:30am to 5:00pm.

If attempts to reach the Examiner by telephone are

Unsuccessful, the Examiner's supervisor, Seema S. Rao can be reached on (571) 270-3174. The fax phone number for the organization where this application or proceeding is assigned is (571) 273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system.

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(EBC) at 866-217-9197 (toll-free) or 571-272-4100.

Any inquiry of a general nature or relating to the status of this

application or proceeding should be directed to the receptionist/

customer service whose telephone number is (571) 272-2600.

Syed Zaidi

Syed S. Yard

S.Z/sz

November 15, 2007.

SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2600